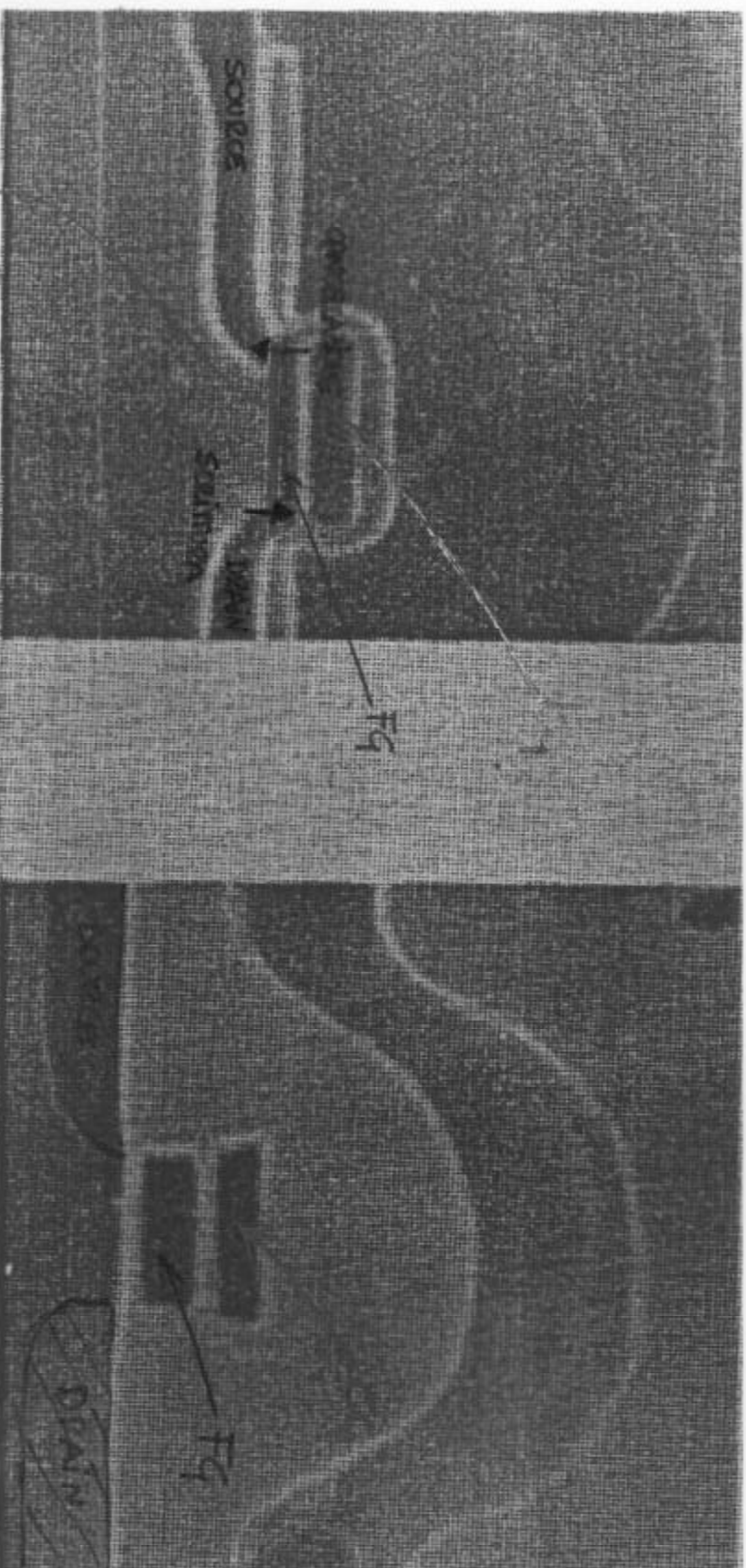


Cross-sections of NVM cells



Oxidizing
TUNNEL ($< 10 \text{ nm}$)
Flash

Digital Integrated Circuits

Courtesy Intel

Memory

EPROM

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